

ABSTRACT OF THE DISCLOSURE

Analog circuitry is disclosed for conditioning a signal from linear and rotary variable differential transformers having a primary winding, a pair of secondary windings and a movable core. The circuitry includes a unique closed loop negative feedback mechanism which, over time, adjusts the frequency of a voltage controlled oscillator so that a steady-state condition is achieved within the circuitry, whereby the total integrated value of the secondary winding voltages at an integrator remains equal to a reference voltage, thereby reducing the computational burden associated with solving an equation to determine the position of the movable core of the transformer.

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